

Docket No. 250923US-2S

IN RE APPLICATION OF: Motohiro ENKAKU

SERIAL NO: 10/806,244

FILED: March 23, 2004

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND ERROR DETECTING METHOD THEREFOR



*ITFAP*

COMMISSIONER FOR PATENTS  
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

☐ No additional fee is required

☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.

☒ Additional documents filed herewith: Petition for Extension of Time (3 months), Letter Submitting Replacement Drawing Sheet(s), Replacement Sheet (1)

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	17	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	4	MINUS	3	1	x \$200 =	\$200.00
APPLICATION SIZE		MINUS	100	0 (each addtl. 50 sheets)	x \$250 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$360 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$200.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		TOTAL				\$200.00

☐ A check in the amount of \$0.00 is attached.

☒ Credit card payment form is attached to cover the fees in the amount of \$1,220.00

☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

*Surinder Sachar*

Eckhard H. Kuesters

Registration No. 28,870

Customer Number

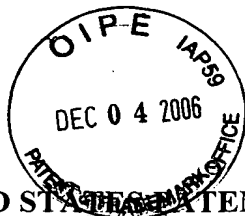
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Surinder Sachar

Registration No. 34,423

Docket No.: 250923US-2S



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Motohiro ENKAKU

SERIAL NO.: 10/806,244

GAU: 2138

FILED: March 23, 2004

EXAMINER: RADOSEVICH. S.

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND ERROR DETECTING  
METHOD THEREFOR

**LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)**

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 1 Replacement Drawing Sheet ☐ \_\_\_\_\_ New Drawing Sheets

☒ Official Action dated June 2, 2006

☐ Notice of Allowance/Issue Fee dated \_\_\_\_\_

☐ Other dated \_\_\_\_\_

The changes and/or modifications made include the following:

A replacement Figure 25 is submitted herein that provides labels beyond the numerical numbering, as requested in the Office Action.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,  
MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters  
Registration No. 28,870

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